Hybrid computing: CPU + GPU co-processing and its application to tomographic reconstruction

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Abstract
Modern computers are equipped with powerful computing engines like multicore processors and GPUs. The 3DEM community has rapidly adapted to this scenario and many software packages now make use of high performance computing techniques to exploit these devices. However, the implementations thus far are purely focused on either GPUs or CPUs. This work presents a hybrid approach that collaboratively combines the GPUs and CPUs available in a computer and applies it to the problem of tomographic reconstruction. Proper orchestration of workload in such a heterogeneous system is an issue. Here we use an on-demand strategy whereby the computing devices request a new piece of work to do when idle. Our hybrid approach thus takes advantage of the whole computing power available in modern computers and further reduces the processing time. This CPU + GPU co-processing can be readily extended to other image processing tasks in 3DEM.

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1. Introduction

High performance computing (HPC) plays a major role in three-dimensional electron microscopy (3DEM) as it allows successfully dealing with the increasing complexity of algorithms and the amount of data needed to push the resolution limits [1]. Different types of HPC platforms have historically been used in 3DEM [1]. In the last few years, graphics processing units (GPUs) have shaken up the HPC field because of their tremendously powerful computing power at an incomparable performance-to-cost ratio [2]. The 3DEM community has rapidly adopted them and many methods and codes have been ported to take advantage of these devices [e.g. 3–5]. In addition, the advent of multicore processors is providing standard computers with awesome computing power, easily exploitable by means of classical HPC strategies [6], which is approaching GPU performance [7]. Some works in 3DEM have already pointed out the importance of these platforms [8,9].

The problem of 3D reconstruction in electron tomography (ET) has long been studied from the HPC perspective with great interest. The main reason is the fact that iterative reconstruction methods (e.g. SIRT [10]) provide better solutions under noisy and low contrast conditions [11] but at the expense of significant processing time, which has prevented their extensive use. Traditionally, there have been HPC proposals for tomographic reconstruction for supercomputers [12], computational grids [13–15] or clusters [16–19]. The trend has turned toward GPUs, and a number of proposals and improvements have lately been presented in the field [3,20–24], including the use of a multi-GPU approach [25,26]. Recently, our group has shown that current multicore computers may exhibit a performance similar to, or even better than, GPU solutions when code optimization, vector processing and multithreading are exploited [9,27].

This work presents a hybrid computing approach that collaboratively combines the CPUs and GPUs available in a computer, and we have applied it to tomographic reconstruction. This strategy is motivated by the performance of our multicore approach [9,27], which showed that a multicore computer and a GPU might be considered as peers. Combining these two computing engines could thus harness and make the most of all computing power latent in standard desktop computers or servers.

2. Hybrid CPU + GPU approach in tomographic reconstruction

CPU cores and GPUs present significant differences in their internal architecture, exploit different levels of parallelism and work at a different pace [6]. The architecture of a system
comprising multicore processors and GPUs is thus heterogeneous. Then, proper distribution of the workload among the various processing elements is an issue, if maximum exploitation is intended [28,29]. Therefore, there is a need for an efficient mechanism to orchestrate the workload in order to maximize the use of the processing elements (CPU cores and GPUs) while minimizing latencies and waiting times. In the HPC field, load balancing has been a subject of research for long [28]. One dynamic load balancing technique that has turned out to be effective consists in splitting the problem into tasks, which are assigned to the processing elements as they become idle (hence ‘dynamic’) [28]. In our hybrid CPU+GPU approach, we use this strategy to dynamically distribute the workload among the CPU cores and the GPUs.

The single-tilt axis geometry typically used in ET allows decomposition of the 3D reconstruction problem into a set of independent 2D reconstruction sub-problems corresponding to the slices perpendicular to the tilt axis. This data decomposition has often been used in the field for 3D reconstruction with HPC techniques [e.g. 1,9,12,13,17]. The independent 2D slices of the volume can then be computed in parallel by the different processing elements using any reconstruction method (WBP, SIRT, etc.). To ameliorate latencies and overheads, the slices are usually packaged into slabs [15]. In particular, in our multicore approach, and in this work, the slices are clustered in slabs of four slices [9]. This is because vector instructions (SSE, Streaming SIMD Extensions) in the CPU cores are exploited to reconstruct four slices simultaneously [27].

Our hybrid approach to tomographic reconstruction then works as follows. A pool of slabs to reconstruct (each slab containing four slices) is maintained. A number of threads run concurrently in the system at the CPU level (so-called C-threads). In addition, for each GPU in the system a specific thread (so-called G-threads) is created, which will be in charge of sending out the projection data (sinograms) to the GPU and receiving the reconstructed slices with the CUDA function cudaMemcpy(). As the threads become idle, they are assigned a new slab to reconstruct. When a slab is allotted to a C-thread, the calculations will be performed on one of the CPU cores, and the four slices in the slab will be computed simultaneously thanks to the vector instructions. In the case of the G-threads, the calculations will be carried out on the GPU using fine-grain parallelism at the level of the voxel. When a thread (either C- or G-) finishes its work, it requests another slab to reconstruct. This asynchronous dispatching of workload on-demand is the implementation of the dynamic load balancing technique referred to above. As the GPU normally makes the computations much faster than a single CPU core, a G-thread will be allotted more slabs than a C-thread. Fig. 1 shows an illustrative scheme of our hybrid CPU+GPU approach to tomographic reconstruction.

3. Results

To evaluate our hybrid implementation, we have chosen two different platforms. The first one is an eight-core machine (two quad-core processors Intel Xeon E5640 at 2.66 GHz) equipped with two GPUs Nvidia Tesla C2050, each GPU with 448 cores at 1.15 GHz and 2.6 GB memory. The second platform is a quad-core machine (based on an Intel Xeon W3520 at 2.66 GHz) and a GPU Nvidia GTX285 with 240 cores at 1.4 GHz and 2 GB memory. The two most common ET reconstruction methods, WBP and SIRT.

![Fig. 1. Hybrid CPU + GPU approach to tomographic reconstruction. The system keeps a pool of slabs of four slices to be reconstructed. A number of threads to be mapped to CPU cores (denoted by C-threads) are running concurrently in the system. Also, specific threads (denoted by G-threads) in charge of the tasks to be computed on the GPUs are also running. The slabs are asynchronously dispatched to the threads on-demand. In the figure, allocation of slabs to threads are color-coded. Note that the G-threads will request tasks more often than the C-threads as GPUs make the calculations faster than a single CPU core. (For interpretation of the references to color in this figure legend, the reader is referred to the web version of this article.)](image)
(30 iterations), have been tested. For the GPU, we have chosen the matrix approach of these methods as it proves to be one of the fastest implementations so far \cite{22,23}. For the CPU, our fast multithreaded vectorized and optimized implementation has been used \cite{9,27}. We have selected three representative sizes for the datasets: tilt-series of 122 images of $1024 \times 1024$, $2048 \times 2048$, and $4096 \times 4096$ to yield reconstructions of the same size in X and Y, and thickness 256. In the particular case of the quad-core machine, the GPU did not have enough memory to accommodate the matrix requirements for the largest (4096) case, so a volume of $4096 \times 4096 \times 128$ was reconstructed instead. The experiments were executed five times and the average time was selected for analysis.

For the evaluation, several types of experiments were carried out and reported here: (1) the pure multicore approach using all cores available in the platform, (2) the pure multiGPU/GPU strategy exploiting all the GPUs in the platform, (3) the hybrid approach launching threads for all the CPU cores and all GPUs (i.e. eight C-threads and two G-threads in Platform 1 – denoted by 8C/2G – and 4C/1G in Platform 2) and (4) the hybrid approach launching G-threads for all the GPUs but keeping the total sum of threads no higher than the CPU-cores available (i.e. 6C/2G in Platform 1 and 3C/1G in Platform 2). Additional experiments were conducted in this study. For brevity, these results are not included here because with less C-threads than the configurations tested here, the performance was poorer. In the case of Platform 1, we also tested configurations involving only one GPU (i.e. 8C/1G, 7C/1G, 6C/1G), which showed the same behavior as that reported here.

Tables 1 and 2 summarize the results obtained for Platform 1 with WBP and SIRT, respectively, whereas Tables 3 and 4 refer to the second platform. In the tables, the processing time required for the reconstruction as well as the distribution of workload (i.e. how many slices have been reconstructed by the CPUs and by the

**Table 1**

<table>
<thead>
<tr>
<th>Configuration</th>
<th>1024</th>
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<tr>
<td>(C-/G-threads)</td>
<td>T.Rec. (s)</td>
<td>Slices (%)</td>
<td>CPU/GPU</td>
<td>T.Rec. (s)</td>
<td>Slices (%)</td>
<td>CPU/GPU</td>
<td>T.Rec. (s)</td>
<td>Slices (%)</td>
<td>CPU/GPU</td>
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<td>100.0/ - - -</td>
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<td>51.84</td>
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<td>CPU/GPU</td>
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<td>- - -/100.0</td>
<td>CPU/GPU</td>
<td>41.14</td>
<td>- - -/100.0</td>
<td>CPU/GPU</td>
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<td>7.17</td>
<td>54.00/46.00</td>
<td>CPU/GPU</td>
<td>30.30</td>
<td>52.10/47.90</td>
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<td>6.43</td>
<td>41.85/58.15</td>
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<td>28.27</td>
<td>41.50/58.50</td>
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**Table 2**

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<td>Slices (%)</td>
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<td>T.Rec. (s)</td>
<td>Slices (%)</td>
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<td>390.11</td>
<td>52.30/47.70</td>
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<tr>
<td>Hybrid (6C/2G)</td>
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<td>46.88/53.12</td>
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<td>46.88/53.12</td>
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<td>47.19/52.81</td>
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**Table 3**

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<td>CPU/GPU</td>
<td>T.Rec. (s)</td>
<td>Slices (%)</td>
<td>CPU/GPU</td>
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<td>CPU/GPU</td>
<td>29.50</td>
<td>- - -/100.0</td>
<td>CPU/GPU</td>
<td>73.97</td>
<td>- - -/100.0</td>
<td>CPU/GPU</td>
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<tr>
<td>Hybrid (4C/1G)</td>
<td>3.77</td>
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<td>CPU/GPU</td>
<td>15.32</td>
<td>60.35/39.65</td>
<td>CPU/GPU</td>
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<td>63.77/36.23</td>
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<tr>
<td>Hybrid (3C/1G)</td>
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<td>56.64/43.36</td>
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<td>14.84</td>
<td>53.13/46.87</td>
<td>CPU/GPU</td>
<td>33.38</td>
<td>56.25/43.75</td>
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**Table 4**

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<th>Configuration</th>
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<td>(C-/G-threads)</td>
<td>T.Rec. (s)</td>
<td>Slices (%)</td>
<td>CPU/GPU</td>
<td>T.Rec. (s)</td>
<td>Slices (%)</td>
<td>CPU/GPU</td>
<td>T.Rec. (s)</td>
<td>Slices (%)</td>
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<td>CPU/GPU</td>
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<td>100.0/ - - -</td>
<td>CPU/GPU</td>
<td>2499.48</td>
<td>100.0/ - - -</td>
<td>CPU/GPU</td>
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<tr>
<td>Pure GPU (1G)</td>
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<td>- - -/100.0</td>
<td>CPU/GPU</td>
<td>1621.24</td>
<td>- - -/100.0</td>
<td>CPU/GPU</td>
<td>3195.90</td>
<td>- - -/100.0</td>
<td>CPU/GPU</td>
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<tr>
<td>Hybrid (4C/1G)</td>
<td>213.27</td>
<td>60.94/39.06</td>
<td>CPU/GPU</td>
<td>845.07</td>
<td>59.18/40.82</td>
<td>CPU/GPU</td>
<td>1715.80</td>
<td>58.40/41.60</td>
<td>CPU/GPU</td>
</tr>
<tr>
<td>Hybrid (3C/1G)</td>
<td>205.40</td>
<td>56.12/43.88</td>
<td>CPU/GPU</td>
<td>792.88</td>
<td>51.56/48.44</td>
<td>CPU/GPU</td>
<td>1617.22</td>
<td>51.56/48.44</td>
<td>CPU/GPU</td>
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GPUs) are presented. The latter is expressed in percentage to allow easy comparison between the different dataset sizes and configurations.

The hybrid approach (any of the two configurations) clearly outperforms the pure CPU or pure GPUs strategies in terms of processing time, regardless of the platform and reconstruction method, as shown by Tables 1–4. Some processing times are especially remarkable: a SIRT reconstruction of a 2048 dataset, which is currently a standard case in the ET field, only requires 6 and 13 min in Platforms 1 and 2, respectively. Interestingly, all those results point out that the second configuration of the hybrid approach is better than the first one. This means that, for maximum performance, it is important to keep the total number of threads running in the system no higher than the number of CPU-cores in the platform. In order to analyze the net gain of the hybrid approach with regard to the standard pure CPU or GPU strategies, we computed the average speed-up factor taking into account all the dataset sizes and reconstruction methods. Fig. 2(left) shows that, compared to the pure multicore implementation, the acceleration factor exhibited by the hybrid approach is around 1.5. When compared to the multiGPU (Platform 1) or GPU (Platform 2) strategy, the speed-up factor ranges from 1.5 to 2.0 (Fig. 2(right)). Finally, restricting the total number of threads to the number of CPU-cores involves a further improvement in the speed-up factor that amounts to 5–8% as shown in Fig. 2.

As far as distribution of workload is concerned, from the data in Tables 1–4 the global averages were computed and charted in Fig. 3. It can be observed that, in Platform 1 and the first hybrid configuration (8C/2G), the CPU-cores are in charge of around 55% of the slices whereas the remaining 45% are for the two GPUs. In Platform 2 using 4C/1G, the CPUs reconstruct around 60% of the slices while the GPU gets 40%. When the total number of threads is restricted (second hybrid configuration), a re-distribution of the workload toward the GPUs is seen (around 45% CPU, 55% GPUs in Platform 1; around 55% CPU, 45% GPU in Platform 2). That is, each GPU gets around 5% more work than with the first configuration. Fig. 4 plots the average workload per thread. It proves that a G-thread processes substantially more slices than a single C-thread. Interestingly, it can be observed that in the second hybrid configuration not only the G-threads get more workload, but the C-threads actually increase their burden as well, though at lower rate (around 1% and 3% in the two platforms, respectively, compared to the 5% of the G-threads).

Finally, we wanted to compare our hybrid approach with a recent powerful distributed multiGPU scheme [25]. This is another hybrid strategy working at a higher level, which exploits several GPUs distributed across different computers. Thus, we performed a set of experiments consisting of 10 iterations of SIRT with the three datasets, and measuring the reconstruction time and the wall-time (i.e. the total time including I/O and all other system overhead). We restricted ourselves to our most powerful machine, Platform 1, and the best configuration 6C/2G. Table 5 shows the results we obtained. The times reported by [25] with 10 GPUs distributed across three computers with datasets with the same size are also reproduced. In comparison to that work, our approach turns out to be very well positioned. In particular, the performance is exceptionally good for large sizes. There the time for reconstruction (T.Rec.) is lower, but the point that really makes difference is the system overhead. For a reconstruction of 4096 × 4096 × 256 from 122 images, the scheme by [25] spends 10 min in system overhead, whereas our approach reduces this time to less than 2 min. Obviously, the transmission of data and reconstructed slabs through the network dramatically penalizes the distributed GPU scheme. In our hybrid approach, however, all I/O operations remain at a local level.
4. Discussion and conclusion

This work presents a hybrid computing approach, combining GPUs and multicore processors, to fully take advantage of the computing power latent in modern computers. It also presents its application to the problem of tomographic reconstruction. One inherent characteristic of these modern platforms is their heterogeneity, which raises the issue of distribution of the workload among the different processing elements. Adaptive load balancing techniques are thus necessary to properly adjust the amount of work to be done by each computing element. Here we have chosen the ‘on-demand’ strategy, a well-known technique in the HPC field, by which the different elements asynchronously request a piece of work to do when they become idle, thereby keeping the system fairly well balanced.

Our hybrid approach improves the performance by a factor in the range 1.5 to 2 with regard to the standard strategies based on pure CPU, GPU or multiGPUs. Although these figures are relative to the systems and implementations that were tested here, they are useful to illustrate the potential of the technique. In platforms with a higher number of GPUs, more powerful GPUs, or with better GPU implementations, our hybrid approach will be capable of adapting to the system by assigning more work to the GPUs. On the contrary, if modest GPUs or CPU implementations are used, it will allot more work to the multiple CPU cores in the system. Furthermore, this dynamic load balancing technique may readily accommodate to a variety of heterogeneous platforms, for instance when several GPUs with different computing capabilities are connected to a multicore computer.

The results we have obtained clearly point out that, for the sake of performance, it is important to limit the total number of threads (the sum of C-threads and G-threads) running in the system to the number of CPU-cores, regardless of the number of GPUs available. This limitation involves a further reduction of the processing time. This point is caused by the fact that, even though the work allotted to the G-threads is to be done on the GPUs, there is still some work to do by the CPU, e.g., the transfer of data and the results between CPU and GPU. For that reason, G-threads actually compete with the C-threads. Limitation of the total number of threads to the CPU cores available minimizes such competition. The use of more sophisticated strategies to reduce communication latencies [26] would also help.

Our hybrid approach fully exploits the processing resources available in modern computers at a local level. When compared to other complex hybrid systems, like the recent distributed multiGPU strategy [25], our approach is well positioned. One of the other complex hybrid systems, like the recent distributed multiGPUs available in modern computers at a local level. When compared to the system configured by [25] is the scalability, in the sense that additional nodes with more GPUs are easily added to the system so as to cope with increasing computational demands. Nevertheless, the high network overhead dramatically reduces its potential scalability. On the contrary, working at a local level like in this hybrid approach, the system overhead is remarkably reduced. The two strategies are not incompatible at all. A hierarchical hybrid system based on distributed nodes, which took advantage of the multiple CPU cores and multiple GPUs within the nodes, could maximize the computational resources while ameliorating network overhead.

The application of hybrid computing to jointly exploit CPUs and GPUs in different image processing tasks involved in the 3DEM disciplines is expected to be straightforward as happened with GPU computing thus far. In a nice recent review, the paramount role that hybrid approaches (i.e. integrative use of different methodologies) play in structural biology was highlighted [30]. As far as exploitation of computing technologies is concerned, the future looks hybrid as well.

The program for tomographic reconstruction using CPU+GPU co-processing is freely available through the following web site: http://www.cnb.csic.es/~jfernandez/hybridcomputing.

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